

In the Specification:

Kindly amend the Title of the Invention on page 1 and page 34 as set forth below:

~~CONTROLLER FOR A POWER CONVERTER AND A
METHOD OF CONTROLLING A SWITCH THEREOF~~
DIGITAL CONTROLLER FOR A POWER CONVERTER EMPLOYING
SELECTABLE PHASES OF A CLOCK SIGNAL

Kindly amend paragraph 35, beginning on page 13, as set forth below:

[0035] Thus, the sparse ADC 140 determines a difference between the output voltage V_{out} and the desired system voltage V_{system} and provides the error signal S_E therefrom. The duty cycle processor 150 then employs the error signal S_E to provide a digital duty cycle signal S_D (e.g., a four or an eight bit digital signal representing a duty cycle) to control a duty cycle of at least one switch of the power converter. An embodiment of a sparse ADC 140 and duty cycle processor 150 are disclosed in U.S. Patent Application Publication No. 2005/0169024 Serial No. [Attorney Docket No. ENP-0011], entitled "Controller for a Power Converter and a Method of Controlling a Switch Thereof," to Dwarakanath, *et al.*, which is incorporated herein by reference.

Kindly amend paragraph 39, beginning on page 15, as set forth below:

[0039] There are a number of viable alternatives to implement a driver 190 that include techniques to provide sufficient signal delays to prevent crosscurrents when controlling multiple switches in the power converter. Of course, any driver 190 capable of providing a drive signal S_{DRV} to control a switch is well within the broad scope of the present invention. Additionally, an embodiment of a driver is disclosed in U.S. Patent Application Publication No. 2005/0168203

~~Serial No. [Attorney Docket No. ENP-003]~~, entitled "Driver for a Power Converter and Method of Driving a Switch Thereof," to Dwarakanath, *et al.*, which is incorporated herein by reference.

Kindly amend paragraph 64, beginning on page 25, as set forth below:

[0064] Thus, a controller for, and related method of, controlling a duty cycle for at least one switch of a power converter with readily attainable and quantifiable advantages has been introduced. Those skilled in the art should understand that the previously described embodiments of the controller, related method, and power converter employing the same are submitted for illustrative purposes only and that other embodiments capable of producing a time-based signal representing a duty cycle for a power converter from a digital word and multiple phases of an oscillator are well within the broad scope of the present invention. Additionally, in an advantageous embodiment, a power converter constructed according to the principles of the present invention may be embodied in an integrated circuit. Alternatively, portions of the power converter such as the controller and the power train (or portions thereof) may also be embodied in an integrated circuit and still be within the broad scope of the present invention. In accordance therewith, selected switches or other devices of the power converter may be embodied in a semiconductor device as disclosed in U.S. Patent Application Publication No. 2005/0167756 ~~Serial No. [Attorney Docket No. ENP-004]~~, entitled "Laterally Diffused Metal Oxide Semiconductor Device and Method of Forming the Same," to Lofti, *et al.*, which is incorporated herein by reference.